High-stability Power Supply System For SuperKEKB Final Focus Superconducting Magnets

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6th Power Converters for Particle Accelerators (POCPA)

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History of peak luminosity (e+/e- colliders)



What is the SuperKEKB

- KEKB: The world's highest luminosity electron-positron collider.
- SuperKEKB: aiming 40 times higher luminosity.



Final focus superconducting magnets around IP

Completely redesigned for SuperKEKB



System overview



System consist of

- SC magnets, quench detection and cooling control system,
- Magnet power supplies,
- AC power distribution board,
- Interlock integration and distribution system,
- Current monitor system and
- Remote control system.

The system is integrated with other systems.

Cabling works

SC magnets and Belle II at IP



To reach the SC magnets, water cooled hollow conductors are installed through a concrete bridge.



Under the bridge, they were connected with power cables.



To much cabling works was carried out

The power cables come from IP to the D2 power supply building.



AC power distribution station

AC power is distributed by newly installed board, where some breakers or transformer in the primary line were also renewed.



Interlock integration/distribution system and current monitor system



Yokogawa FA-M3 PLC Keithley 2002 DMM with 7001 switch sys. Espec SU-642

Installed power supplies

main quad. PSs Constant temp. cabinet for DCCTs and DMMS







List of SC magnet power supplies

Rated output	# of PSs	Load	
2 kA, 15 V	8	Main quad.	
±70 A, ±10 V	43 (+2 spare)	correction coil	
410 A, 15+15 V	1	ESL	
455 A, 15+30 V	1	ESR1	
155 A, 15 V	1	ESR23	

Special power supplies for ...

- Main quadrupole magnet:
 2 ppm ultra high stability and 1 ppm low ripple.
- Correction coils:
 5 ppm high stability with low cost.
- Anti-solenoid:

ESL and ESR1 has a middle tap. Current for each two sections divided by the tap should be controlled independently.

Development of main quad. power supply

Aiming spec.

Rated output	DC 2 kA, 15 V
Current setting resolution	< 0.1 ppm
Current stability	< 2 ppm/8 hrs.
Current ripple (< 10kHz)	< 1 ppm (rms)
Current noise (> 10kHz)	< 1 ppm (0-peak)

R&D items

- 1. High current setting resolution
- 2. High stability
- 3. low ripple

R&D item 1: High current setting resolution

• Example of step response: 16-bit KEKB PS (1 kA, 700 V)





- Aiming 2 ppm stability, 0.1 ppm of setting resolution is required.
- 24-bit control board is developed using two 20-bit DACs. Analog Devices AD5791 is suitable due to its monotonicity spec.

R&D item 1: Developed 24-bit board



R&D item 1: Test result of 24-bit board

• Increasing DAC input digital value by 1 LSB, DAC output voltage is measured by Keithley 2002 DMM.



Monotonic 1 LSB response, that is corresponds to 0.6 $\mu\text{V}/10$ V F.S, is obtained.

R&D item 2: High stability

• Example of stability: KEKB SC Main Q. PS (4 kA, 15 V)



- Typ. stability is 10 ppm (peak to peak)/week.
- Resulting in 1 ppm/K of temp. coeff., although a temp. controlled box and low temp. coeff. parts were used.
- In order to suppress such a fluctuation in the output current, following digital feedback control is developed.

R&D item 2: Digital feedback control



R&D item 2: Test result of the digital feedback loop

• Performed by using a medium-class power supply (15 V, 500 A).



1 ppm / h stability with low σ = 0.16 ppm

For high precision current measurement

- Low temp. coefficients equipment.
 0.3 ppm/K (Keithley 2002), 1.5 ppm/K (TOPACC DCCT)
- Suitable grounding is a key point: 100 GΩ input impedance (DMM), 0.1 ppm =10 µV



+08.0101205 VDC Ranse: 20 VDC

Ground connection should be separated each other.

R&D item 3: Low ripple for low impedance load

• Example of the load impedance (QC1LP)



- Normal-mode impedance < several tens of Ω in the low freq. (<10 kHz) range: Possibility of large current ripple.
- Common-mode impedance < several tens of Ω in the high freq. (>10 kHz) range: Possibility of large switching noise.
- Symmetrical design of circuit is essential to reduce ripple and noise.

R&D item 3: Symmetric circuit design



- DCCTs on both output terminals.
- Symmetric circuit structure
 - with respect to ground.
 - Shielded high-frequency

transformer





Full power test: quench protection trip test (QC1LP)



• Good agreement with estimations Decay time constant : 14.2 ms=0.9 mH / (3.5 m Ω + 60 m Ω) magnet cable + protection R Induced voltage V_{PF}-V_{NF} : 96 V=1.6 kA×60 m Ω

Performance test results 1: QC1LP current ripple

• Dividing the measured voltage ripple by the magnitude of the load impedance, the current ripple is obtained.



- Less than 1 ppm of normal-mode ripple and noise.
- Common-mode components are also measured: 0.5 ppm@57 kHz in the maximum.

Performance test results 2: QC2RE stability



- ~1.5 ppm / 10.5 hrs of current stability is obtained.
- The rapid change in AC voltage leads to makes stability worse.

Feedback control of the DC voltage for chopper



Stability of main bending magnet PSs (860A, 1.1kV)



• 24-bit board and digital FB control are employed similar to SC PS.

- \cdot 2.0 \sim 3.5 ppm/2weeks stability. No spike due to switch mode rectifier.
- Good repeatability after 8 hrs. of power off. and initializing operation.

Another type of 24-bit board assuring monotonicity



Summary

- SuperKEKB is an electron-positron collider that aims 40 times higher peak luminosity than one of the past KEKB.
- Final focus superconducting magnets are key devices to acheve such a high luminosity, and a high stability and a low ripple specifications for its output current are required.
- The developed power supply empeloyed a 24-bits control board, a digital feedback control scheme, and a symmetric circuit design.
- In the test results, 1.5 ppm / 10.5 hrs of current stability and less than 1 ppm ripple are obtained.
- These power supples were applied to the SuperKEKB collision operation and worked well. Further improvement is necessity to reduce spikes due to a rapid change in AC voltage.

Back-up slide



AD7846K (16-bit): DNL ±0.5 LSB max INL ±2 LSB AD5791B (20-bit): DNL ±0.75 LSB typ. (test result: <±0.1 LSB) INL ±0.5 LSB typ. (test result: -0.2~+0.6 LSB)

Why sixteen DAC's 24-bit system?

Care must be taken to the monotonicity of two DAC's 24-bit system



SC corrector power supply

45 of SC corrector power supplies was fabricated.

Rated output	DC \pm 70 A, 10 V
Current setting resolution	< 1 ppm
Current stability	< 5 ppm/8 hrs.
Current ripple (< 10kHz)	< 5 ppm (rms)
Current noise (> 10kHz)	< 5 ppm (0-peak)

Test results: Stability 2.1 ppm/24 hrs. with temp. coeff. of 0.6 ppm/℃



Ripple, noise, quench protection test and so on: also OK.

超伝導補正電磁石電源でのDCCTのコストダウン

仕様:60 A-5 V 5 ppm/8時間

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・TOPACC(88万円/台) ・計3台(制御×2、モニタ×1)



・電源45台分:11,880万円

改造後



・MACC Plus (11万円/台) ・計2台(制御×1、モニタ× 1)



・電源45台分:990万円

主要構成要素の温度係数と恒温槽温度変化

主要構成要素の温度係数(仕様値)

- 0.125 ppm/K (AD5791 DAC with Buffer amps.)
- 0.5 ppm/K (TOPACC optionalの場合:標準は1.5 ppm/K)

243

24.4 -243 -30

2014/07/01

0.3 ppm/K (Keithley 2002)

恒温槽(espec LU-123)の温度変化(2日間実測)

・周囲温度変化 5.5 ℃に対して、 恒温槽内温度変化 0.2 ℃

デジタル帰還制御の温度係数の見積

- ・(0.5+0.3)×0.2=0.16 ppm程度
- ・(1.5+0.3)×0.2=0.36 ppm程度



2014/07/01



2014/07/00 00:00:00

12:00:00

DMM (keithley2002) のノイズ幅 (測定値)

・入力を短絡し、設定を変えて調べた。(ゼロ調整は未実施)



・まとめ

-			測定結果(μV)		仕様	
_	レンジ PLC		標準偏差 σ×2√2		(µVrms)	
_	20	10	0.8	2.3	0.6	
	20	1	1.7	4.7	1.6	
	200	10	17	47	20	
	200	1	37	105	50	

仕様とよく合っている。

10 Vの0.1 ppm = 1 μV

20 Vレンジ、10 PLCなら、0.1 ppm以下の制御に使える

DAC出力の温度係数、変動幅の測定

・DAC AD5791+バッファアンプ AD8675、8676:DMMで測定(20 V, 10PLC



ゆらぎ幅:1ppm/2時間

DCCT・DMMの測定誤差、温度係数(仕様値)

DCCT: Hitec社 TOPACC(10 Vフルスケール、帯域: 500 kHz)

- ・リップル: 0.3 ppm (<100 Hz), 1.5 ppm (<10 kHz)
- ・温度係数: 0.5 ppm/K (TOPACC optionalの場合:標準は1.5 ppm/K)

DMM: Keithley 2002(10 Vに対する値。条件:20V レンジ、10 PLC)

- ・ノイズ幅: 0.06 ppm (rms)
- ・温度係数:0.3 ppm/K

SuperKEKBでのビーム衝突: KEKBと比べて20倍細いビーム

・ナノサイズビームを如何に衝突、維持するか:最も重要な課題の一つ

LERの比較	SuperKEKB	KEKB		
β_x^* / β_y^*	32 mm / 0.27 mm	1200 mm / 5.9 mm		
σ_x^* / σ_y^*	10 µm / 48 nm	147 µm / 940 nm		

・水平方向に衝突ビームがずれた時のルミノシティの減少率(大見氏計算)



βy*を絞るほど、水平方向にずれた時のルミノシティ減少が厳しい

水平方向衝突軌道保持フィードバックシステムとして、PEP-IIで 実績のあるデザリングシステムをSuperKEKBで初めて導入し

ルミノシティ/エネルギーフロンティア







いまここ

- ・2010/6にKEKBの運転を停止して5年にわたる大改修を進めてきた。
- ・Phase 1: 衝突させない周回運転でビーム調整。ビームダクト真空焼き。
- ・Phase 2:衝突運転を始める。ベータを絞り、各種衝突調整を実施。 →「ナノビーム方式」を実証できた。BelleのBGレベルも確認。
- Phase 3: VXDを入れた完全な状態のBelle IIで物理ランを予定。

目標の ピーク・ルミノシティ8×10³⁵ cm⁻²s⁻¹を目指す。

ルミノシティとは

単位時間あたりに生成される物理事象数

ルミノシティ反応断面積・出会いの頻度、輝度
・加速器の性能で決まる×・自然法則で決まる
・ドウシヨウモナイ例: $e^+e^- \to \Upsilon(4S) \to B\overline{B}$ の場合
反応断面積0場合
10⁻³³ cm²
10³⁴ cm⁻² s⁻¹
B・反B中間子が毎秒10ペア生成される

物理事象数を増やすにはルミノシティを上げるしかない。



ルミノシティの式変形:ビーム・ビームパラメータ <mark>そ</mark>の導入

- ・バンチ内粒子数を増やせば、ルミノシティは幾らでも上がるか?
- ・衝突ビームサイズを小さくすれば、…?
- →否:衝突する互いのビームが及ぼしあうカでビームサイズ増大 ・衝突の強さを表す無次元量:ビーム・ビームパラメータ ζ

$$\xi_{\pm y} = \frac{r_e}{2\pi\gamma_{\pm}} \frac{N_{\mp}\beta_y^*}{\sigma_y^*(\sigma_x^* + \sigma_y^*)} R_y$$

←いろいろ制限があって あまり大きくできない

を導入してルミノシティを表現すると…



ベータを絞る制限:砂時計効果

・ルミノシティを上げるためにベータを絞りたい。どこまで絞れるか? →絞りすぎると、衝突点(IP)の前後で砂時計の様にビームが太る。 →せいぜい、ビーム同士が重なる長さ程度しか絞れない。





世界最小 β_v *への挑戦



2018/6/27現在、_{βy}*=2 mmを達成

SuperKEKB加速器の設計パラメーター

パラメータ		KEKB		SuperKEKB		<u> </u>
		LER(e+)	HER(e⁻)	LER	HER	早11/2
ビームエネルギー	Eb	3.5	8	4	7	GeV
半交差角	φ	11		41.5		mrad
水平エミッタンス	εx	18	24	3.2	4.6	nm
エミッタンス比(ɛ _y /ɛ _x)	К	0.88	0.66	0.27	0.25	%
衝突点でのβ関数	βχ/βγ	1200/5.9		32/0.27	25/0.30	mm
ビーム電流	Ib	1.64	1.19	3.6	2.6	А
ビーム・ビーム	ξγ	0.129	0.090	0.088	0.081	
パラメーター						
ルミノシティ	L	2.1 x 10 ³⁴		8 x 10 ³⁵		cm ⁻² s ⁻¹

- ・交差角を大きく、水平ベータ関数を小さくして砂時計効果を緩和
- ・垂直β関数を1/20に絞って、ビーム電流を2倍
- ・まずはKEKBのデザイン値10³⁴を、→40倍のルミノシティを目指す!



KEKB加速器からSuperKEKB加速器への大改修 ー ビーム増強 ー

